用于开漏和推挽应用的2位双向电压电平转换器

UM3202Q *DFN8 1.70×1.35*
UM3202QA *DFN8 1.4×1.0*
UM3202H *CSP8 1.9×0.9*
UM3202V *VSSOP8*
UM3202A *SSOP8*
UM3202S *SOT23-8*

描述

UM3202 是具有±8kV ESD 保护的 2 通道电平转换器，提供允许数据在多电压系统中传输所需的电平转换。外部应用电压 V_{CCB} 和 V_{CCA} 用来在器件任一端侧设置逻辑电平。器件在 V_{CCA} 侧的低压逻辑信号在 V_{CCB} 端侧转为高压逻辑信号，反之亦然。UM3202 为双向电平转换器，利用基于传输门的设计来允许任意单根数据线上的双向(V_{CCA}↔V_{CCB})数据传输。UM3202 的 V_{CCA} 电压范围为+1.65V 至+3.6V; V_{CCB} 电压范围为+2.3 至+5.5V，此系列的芯片理想用于低压 ASIC/PLD 与较高压系统间的数据传输。

当输出使能 (OE) 输入为低电平时，UM3202 进入高阻态输出模式，以减小供电电流。UM3202A 被设计为由 V_{CCA} 为 OE 输入电路供电。芯片在 V_{CCB} 侧具有±8kV ESD 保护，为外部信号切换应用场合提供更强的保护。

UM3202 为 2 通道电平转换器，采用 DFN8 1.70×1.35、DFN8 1.4×1.0、CSP8 1.9×0.9、VSSOP8、SSOP8 和 SOT23-8 封装。

应用

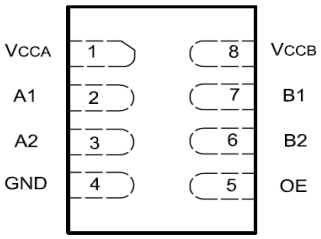
- SPI, MICROWIRE 和 I^2C电平转换
- 低电压ASIC电平转换
- 智能读卡器
- 手机支架
- 便携式POS系统
- 便携式通信设备
- 低成本串行接口
- 蜂窝电话
- GPS
- 通信设备

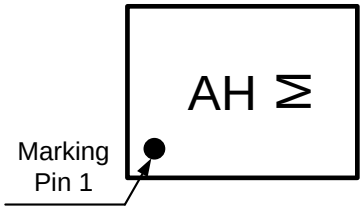
特性

- 最大数据速率：
  24Mbps (推挽)
  2Mbps (开漏)
- 双向电平转换
- A端口输入电压范围为1.65V至3.6V
  B端口输入电压范围为2.3V至5.5V
  (V_{CCA}≤V_{CCB})
- B端口提供±8kV ESD保护
- 无需电源排序，V_{CCA} 或 V_{CCB} 均可优先斜升
- 采用DFN8、CSP8、VSSOP8、SSOP8和SOT23-8封装

Pin Configurations **Top View**

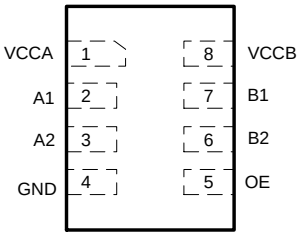
(Top View)

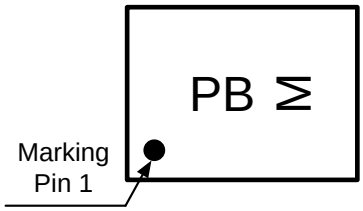




M: Month Code
UM3202Q
DFN8 1.70×1.35

(Top View)





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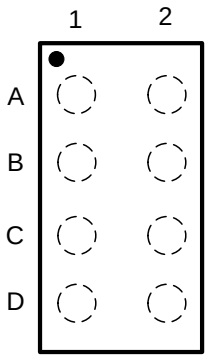
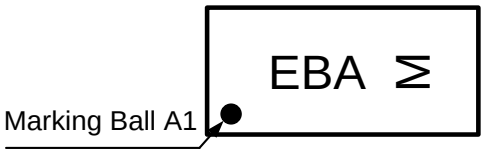
DFN8 1.4×1.0

(Top View)





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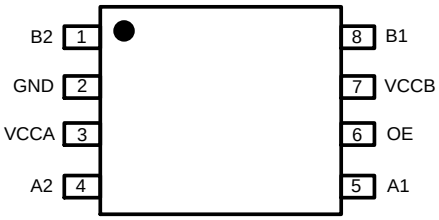
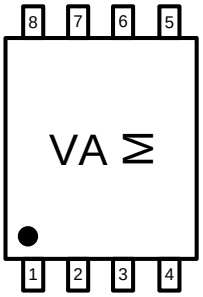
UM3202H
CSP8 1.9×0.9





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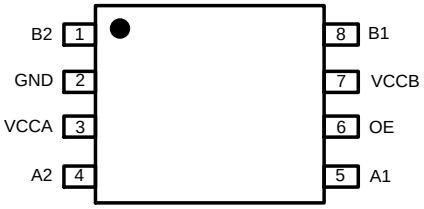
VSSOP8



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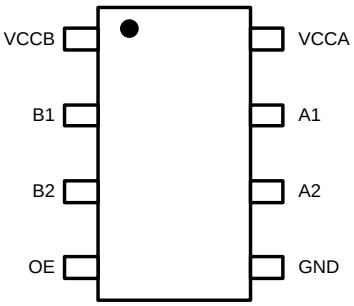
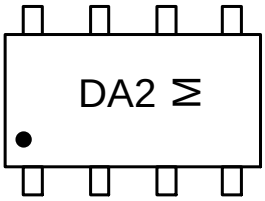
SSOP8





M: Month Code
UM3202S
SOT23-8

Ball Mapping for UM3202H

	1	2
A	B2	B1
B	GND	VCCB
C	VCCA	OE
D	A2	A1

Transparent Top View

Pin Description

Pin Name	Function
V_{CCA}	A-Port Supply Voltage. $1.65V \le V_{CCA} \le 3.6V$ and $V_{CCA} \le V_{CCB}$
A1	Input/Output 1. Referenced to V_{CCA}
A2	Input/Output 2. Referenced to V_{CCA}
GND	Ground
OE	3-State Output Enable. Pull OE low to place all outputs in 3-state mode. Referenced to V_{CCA}
B2	Input/Output 2. Referenced to V_{CCB}
B1	Input/Output 1. Referenced to V_{CCB}
V_{CCB}	B-Port Supply Voltage. $2.3V \le V_{CCB} \le 5.5V$

Ordering Information

Part Number	Packaging Type	Marking Code	Shipping Qty
UM3202Q	DFN8 1.70×1.35	AH	3000pcs/7 Inch Tape & Reel
UM3202QA	DFN8 1.4×1.0	PB	
UM3202H	CSP8 1.9×0.9	EBA	
UM3202V	VSSOP8	VA	
UM3202A	SSOP8	SAC	
UM3202S	SOT23-8	DA2	

Absolute Maximum Ratings (Note 1)

Over operating free-air temperature range (unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{CCA}	Supply Voltage Range		-0.5 to +4.5	V
V_{CCB}	Supply Voltage Range		-0.5 to +6.5	V
V_I	Input Voltage Range	A Ports	-0.5 to +4.5	V
		B Ports	-0.5 to +6.5	
V_O	Voltage Range Applied to Any Output in the High-Impedance or Power-Off State	A Ports	-0.5 to +4.5	V
		B Ports	-0.5 to +6.5	
V_O	Voltage Range Applied to Any Output in the High or Low State (Note 2)	A Ports	-0.5 to (V_{CCA}+0.5)	V
		B Ports	-0.5 to (V_{CCB}+0.5)	
I_{IK}	Input Clamp Current	V_I＜0	-50	mA
I_{OK}	Output Clamp Current	V_O＜0	-50	mA
I_O	Continuous Output Current		±50	mA
	Continuous Current through V_{CCA}, V_{CCB}, or GND		±100	mA
T_{OP}	Operating Temperature Range		-40 to +85	℃
T_{STG}	Storage Temperature Range		-65 to +150	℃

Note 1. Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Note 2. The value of V_{CCA} and V_{CCB} are provided in the recommended operating conditions table.

Thermal Information

Symbol	Parameter		Value	Unit
$R_{\theta JA}$	Junction-to-ambient thermal resistance	DFN8 1.70×1.35	162	℃/W
		DFN8 1.4×1.0	199.3	
		CSP8 1.9×0.9	105	
		VSSOP8	199.3	
		SSOP8	182.6	
		SOT23-8	240	
$R_{\theta JC}$	Junction-to-case thermal resistance	DFN8 1.70×1.35	104	℃/W
		DFN8 1.4×1.0	26.4	
		CSP8 1.9×0.9	71	
		VSSOP8	76.2	
		SSOP8	113.3	
		SOT23-8	175	

Recommended Operating Conditions (Note 1, 2)

Symbol	Parameter		V_{CCA}	V_{CCB}	Min	Max	Unit
V_{CCA}	Supply Voltage				1.65	3.6	V
V_{CCB}					2.3	5.5	
V_{IH}	High Level Input Voltage	A-Port	1.65V to 1.95V	2.3V to 5.5V	V_{CCI}-0.2	V_{CCI}	V
			2.3V to 3.6V		V_{CCI}-0.4	V_{CCI}	
		B-Port	1.65V to 3.6V	2.3V to 5.5V	V_{CCI}-0.4	V_{CCI}	
		OE			V_{CCA}×0.65	5.5	
V_{IL}	Low Level Input Voltage	A-Port	1.65V to 3.6V	2.3V to 5.5V	0	0.15	V
		B-Port			0	0.15	
		OE			0	V_{CCA}×0.35	
Δt/ΔV	Input Transition Rise or Fall Time	A-Port Push-Pull Driving	1.65V to 3.6V	2.3V to 5.5V		10	ns/V
		B-Port Push-Pull Driving				10	
		Control Input				10	

Note 1. V_{CCI} is the supply voltage associated with the input port.
Note 2. V_{CCA} must be less than or equal to V_{CCB} and must not exceed 3.6 V.

Electrical Characteristics (Note 1, 2, 3)

Over recommended operating free-air temperature range (unless otherwise noted)

Parameter		Test Conditions	V_{CCA}	V_{CCB}	T_A=25℃		-40℃ to 85℃		Unit
					Typ	Max	Min	Max	
V_{OHA}		I_{OH}=-20μA	1.65V to 3.6V	2.3V to 5.5V			V_{CCA}×0.8		V
V_{OLA}		I_{OL}=1mA	1.65V to 3.6V	2.3V to 5.5V				0.4	V
V_{OHB}		IOH=-20μA	1.65V to 3.6V	2.3V to 5.5V			V_{CCB}×0.8		V
V_{OLB}		I_{OL}=1mA	1.65V to 3.6V	2.3V to 5.5V				0.4	V
I_I	OE	V_I=V_{CCI} or GND	1.65V to 3.6V	2.3V to 5.5V		±1		±2	μA
I_{OZ}	A or B Port	OE=V_{IL}	1.65V to 3.6V	2.3V to 5.5V		±1		±2	μA
I_{CCA}		V_I=V_O=open, I_O=0	1.65V to V_{CCB}	2.3V to 5.5V				2.4	μA
			3.6V	0V				2.2	
			0V	5.5V				-1	
I_{CCB}		V_I=V_O=open, I_O=0	1.65V to V_{CCB}	2.3V to 5.5V				12	μA
			3.6V	0V				-1	
			0V	5.5V				1	
I_{CCA}+I_{CCB}		V_I=V_O=open, I_O=0	1.65V to 3.6V	2.3V to 5.5V				14.4	μA
C_i	OE		3.3V	3.3V	2.5			3.5	pF
C_{iO}	A Port		3.3V	3.3V	5			6.5	pF
	B Port				12			16.5	

Note 1. V_{CCI} is the supply voltage associated with the input port.
Note 2. V_{CCO} is the supply voltage associated with the output port.
Note 3. V_{CCA} must be less than or equal to V_{CCB} and must not exceed 3.6V.

Timing Requirements

Over recommended operating free-air temperature range, V_{CCA}=1.8V±0.15V (unless otherwise noted)

			V_{CCB}=2.5V ±0.2V		V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
			Min	Max	Min	Max	Min	Max	
Data Rate	Push-Pull Driving			24		24		24	Mbps
	Open-Drain Driving			2		2		2	
t_w Pulse Duration	Push-Pull Driving	Data Inputs	41		41		41		ns
	Open-Drain Driving		500		500		500		

Timing Requirements (Continued)

Over recommended operating free-air temperature range, V_{CCA}=2.5V±0.2V (unless otherwise noted)

			V_{CCB}=2.5V ±0.2V		V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
			Min	Max	Min	Max	Min	Max	
Data Rate	Push-Pull Driving			24		24		24	Mbps
	Open-Drain Driving			2		2		2	
t_w Pulse Duration	Push-Pull Driving	Data Inputs	41		41		41		ns
	Open-Drain Driving		500		500		500		

Timing Requirements (Continued)

Over recommended operating free-air temperature range, V_{CCA}=3.3V±0.3V (unless otherwise noted)

			V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
			Min	Max	Min	Max	
Data Rate	Push-Pull Driving			24		24	Mbps
	Open-Drain Driving			2		2	
t_w Pulse Duration	Push-Pull Driving	Data Inputs	41		41		ns
	Open-Drain Driving		500		500		

Switching Characteristics

Over recommended operating free-air temperature range, V_{CCA}=1.8V±0.15V (unless otherwise noted)

Parameter	From (Input)	To (Output)	Test Conditions	V_{CCB}=2.5V ±0.2V		V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
				Min	Max	Min	Max	Min	Max	
t_{PHL}	A	B	Push-Pull		4.6		4.7		5.8	ns
			Open-Drain	2.9	8.8	2.9	9.6	3	10	
t_{PLH}			Push-Pull		6.8		6.8		7	
			Open-Drain	45	260	36	208	27	198	
t_{PHL}	B	A	Push-Pull		4.4		4.5		4.7	ns
			Open-Drain	1.9	5.3	1.1	4.4	1.2	4	
t_{PLH}			Push-Pull		5.3		4.5		0.5	
			Open-Drain	45	175	36	140	27	102	
t_{en}	OE	A			200		200		200	ns
		B			200		200		200	
t_{dis}	OE	A			50		40		35	ns
		B			50		40		35	
t_{rA}	A Port Rise Time		Push-Pull	3.2	9.5	2.3	9.3	2	7.6	ns
			Open-Drain	38	165	30	132	22	95	
t_{rB}	B Port Rise Time		Push-Pull	4	10.8	2.7	9.1	2.7	7.6	ns
			Open-Drain	34	145	23	106	10	58	
t_{fA}	A Port Fall Time		Push-Pull	2	5.9	1.9	6	1.7	13.3	ns
			Open-Drain	4.4	6.9	4.3	6.4	4.2	6.1	
t_{fB}	B Port Fall Time		Push-Pull	2.9	7.6	2.8	7.5	2.8	8.8	ns
			Open-Drain	6.9	13.8	7.5	16.2	7	16.2	
$t_{SK(O)}$	Channel-to-Channel				1		1		1	ns
Max Data Rate			Push-Pull		24		24		24	Mbps
			Open-Drain		2		2		2	

Switching Characteristics (Continued)

Over recommended operating free-air temperature range, V_{CCA}=2.5V±0.2V (unless otherwise noted)

Parameter	From (Input)	To (Output)	Test Conditions	V_{CCB}=2.5V ±0.2V		V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
				Min	Max	Min	Max	Min	Max	
t_{PHL}	A	B	Push-Pull		3.2		3.3		3.4	ns
			Open-Drain	1.7	6.3	2	6	2.1	5.8	
t_{PLH}			Push-Pull		3.5		4.1		4.4	
			Open-Drain	43	250	36	206	27	190	
t_{PHL}	B	A	Push-Pull		3		3.6		4.3	ns
			Open-Drain	1.8	4.7	2.6	4.2	1.2	4	
t_{PLH}			Push-Pull		2.5		1.6		0.7	
			Open-Drain	44	170	37	140	27	103	
t_{en}	OE	A			200		200		200	ns
		B			200		200		200	
t_{dis}	OE	A			50		40		35	ns
		B			50		40		35	
t_{rA}	A Port Rise Time		Push-Pull	2.8	7.4	2.6	6.6	1.8	5.6	ns
			Open-Drain	34	149	28	121	24	89	
t_{rB}	B Port Rise Time		Push-Pull	3.2	8.3	2.9	7.2	2.4	6.1	ns
			Open-Drain	35	151	24	112	12	64	
t_{fA}	A Port Fall Time		Push-Pull	1.9	5.7	1.9	5.5	1.8	5.3	ns
			Open-Drain	4.4	6.9	4.3	6.2	4.2	5.8	
t_{fB}	B Port Fall Time		Push-Pull	2.2	7.8	2.4	6.7	2.6	6.6	ns
			Open-Drain	5.1	8.8	5.4	9.4	5.4	10.4	
$t_{SK(O)}$	Channel-to-Channel				1		1		1	ns
Max Data Rate			Push-Pull	24		24		24		Mbps
			Open-Drain	2		2		2		

Switching Characteristics (Continued)

Over recommended operating free-air temperature range, V_{CCA}=3.3V±0.3V (unless otherwise noted)

Parameter	From (Input)	To (Output)	Test Conditions	V_{CCB}=3.3V ±0.3V		V_{CCB}=5V ±0.5V		Unit
				Min	Max	Min	Max	
t_{PHL}	A	B	Push-Pull		2.4		3.1	ns
			Open-Drain	1.2	4.2	1.4	4.6	
t_{PLH}			Push-Pull		4.2		4.4	
			Open-Drain	36	204	28	165	
t_{PHL}	B	A	Push-Pull		2.5		3.3	ns
			Open-Drain	1	124	1	97	
t_{PLH}			Push-Pull		2.5		2.6	
			Open-Drain	3	139	3	105	
t_{en}	OE	A			200		200	ns
		B			200		200	
t_{dis}	OE	A			40		35	ns
		B			40		35	
t_{rA}	A Port Rise Time		Push-Pull	2.3	5.6	1.9	4.8	ns
			Open-Drain	25	116	19	85	
t_{rB}	B Port Rise Time		Push-Pull	2.5	6.4	2.1	7.4	ns
			Open-Drain	26	116	14	72	
t_{fA}	A Port Fall Time		Push-Pull	2	5.4	1.9	5	ns
			Open-Drain	4.3	6.1	4.2	5.7	
t_{fB}	B Port Fall Time		Push-Pull	2.3	7.4	2.4	7.6	ns
			Open-Drain	5	7.6	4.8	8.3	
$t_{SK(O)}$	Channel-to-Channel				1		1	ns
Max Data Rate			Push-Pull	24		24		Mbps
			Open-Drain	2		2		

Applications Information

The UM3202 can be used in level-translation applications for interfacing devices or systems operating at different interface voltages with one another. The UM3202 is ideal for use in application where an open-drain driver is connected to the data I/Os. The UM3202 can also be used in applications where a push-pull driver is connected to the data I/Os, but the UM3302 might be a better option for such push-pull applications.

Block Diagram

The UM3202 (block diagram see Figure 1) does not require a direction-control signal to control the direction of data flow from A to B or from B to A. Each A-port I/O has an internal 10kΩ pull-up resistor to V_{CCA}, and each B-port I/O has an internal 10kΩ pull-up resistor to V_{CCB}. During a rising edge, the one-shot turns on the PMOS transistors (PU1, PU2) for a short duration, that speeds up the low-to-high transition.

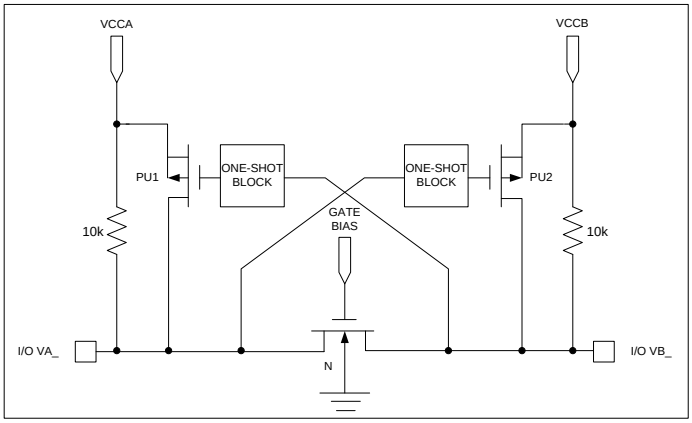


Figure 1 Block Diagram of UM3202 I/O Cell

Input Driver Requirements

The fall time (t_{fA}, t_{fB}) of a signal depends on the output impedance of the external device driving the data I/Os of the UM3202. Similarly, the t_{PHL} and the maximum date rates also depend on the output impedance of the external driver. The values for t_{fA}, t_{fB}, t_{PHL}, and the maximum date rates in the data sheet assume that the output impedance of the external driver is less than 50Ω.

Power Up

During operation, ensure that $V_{CCA} \leq V_{CCB}$ at all times. During power-up sequencing, $V_{CCA} \geq V_{CCB}$ does not damage the device, so any power supply can be ramped up first.

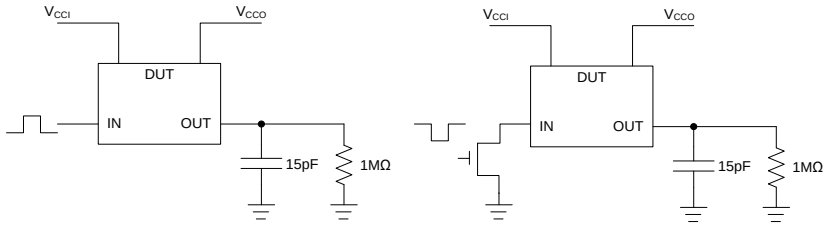
Enable and Disable

The UM3202 has an OE input that is used to disable the device by setting OE=low, which places all I/Os in the high-impedance (Hi-Z) state. The disable time (t_{dis}) indicates the delay between the time when OE goes low and when the outputs actually get disabled (Hi-Z). The enable time (t_{en}) indicates the amount of time the user must allow for the one-shot circuitry to become operational after OE is taken high.

Pull-up or Pull-down Resistors on I/O Lines

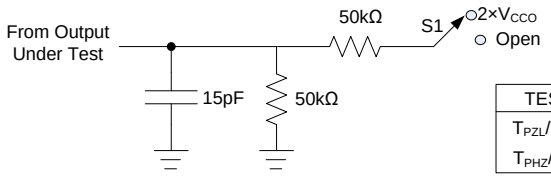
Each A-port I/O has an internal 10kΩ pull-up resistor to V_{CCA}, and each B-port I/O has an internal 10kΩ pull-up resistor to V_{CCB}. If a smaller value of pull-up resistor is required, an external resistor must be added from the I/O to V_{CCA} or V_{CCB} (in parallel with the internal 10kΩ resistor).

Test Circuits



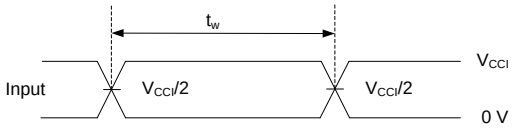
Data Rate, Pulse Duration, Propagation Delay, Output Rise and Fall Time measurement using a push-pull driver

Data Rate, Pulse Duration, Propagation Delay, Output Rise and Fall Time measurement using an open-drain driver

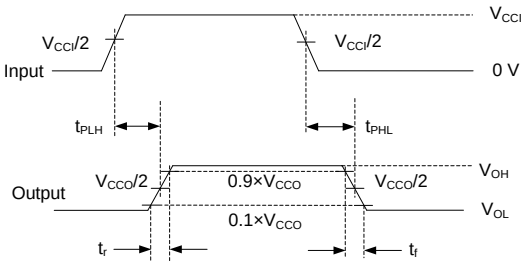


TEST	S1
T_{PZL}/T_{PLZ}	$2\times V_{CCO}$
T_{PHZ}/T_{PZH}	Open

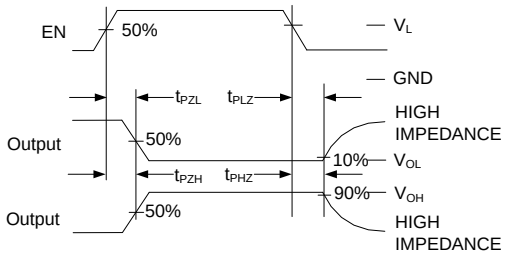
Load Circuit for Enable/Disable Time Measurement



Voltage Waveforms Pulse Duration



Voltage Waveforms Propagation Delay Times



Voltage Waveforms Propagation Disable Times

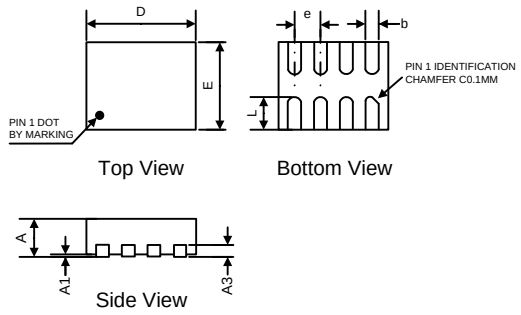
A. C_L includes probe and jig capacitances.
B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.
Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
C. All input pulses are supplied by generators having the following characteristics: PRR≤100MHz, Z_O=50Ω, dV/dt≥1V/ns.
D. The outputs are measured one at a time, with one transition per measurement.
E. T_{PLZ} and T_{PHZ} are the same as t_{dis}.
F. T_{PZL} and T_{PZH} are the same as t_{en}.
G. T_{PLH} and T_{PHL} are the same as t_{pd}.
H. V_{CCI} is the V_{CC} associated with the input port.
I. V_{CCO} is the V_{CC} associated with the output port.
J. All parameters and waveforms are not applicable to all devices.

Figure 2 Load Circuits and Voltage Waveforms

Package Information

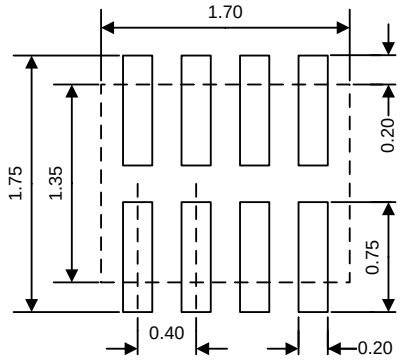
DFN8 1.70×1.35

Outline Drawing



DIMENSIONS						
Symbol	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	0.50	0.55	0.60	0.020	0.022	0.024
A1	0.00	-	0.05	0.000	-	0.002
A3	0.15REF			0.006REF		
b	0.15	0.20	0.25	0.006	0.008	0.010
D	1.65	1.70	1.75	0.065	0.067	0.069
E	1.30	1.35	1.40	0.051	0.053	0.055
e	0.40BSC			0.016BSC		
L	0.45	0.50	0.55	0.018	0.020	0.022

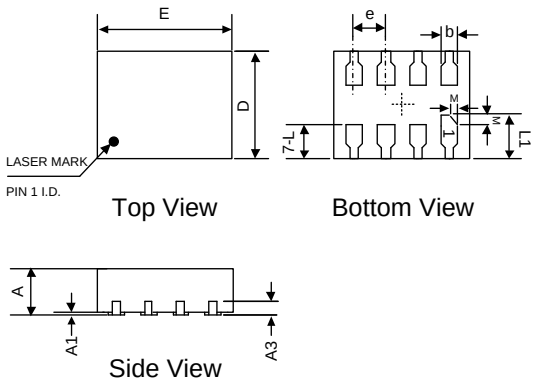
Land Pattern



NOTES:
1. Compound dimension: 1.70×1.35;
2. Unit: mm;
3. General tolerance ±0.05mm unless otherwise specified;
4. The layout is just for reference.

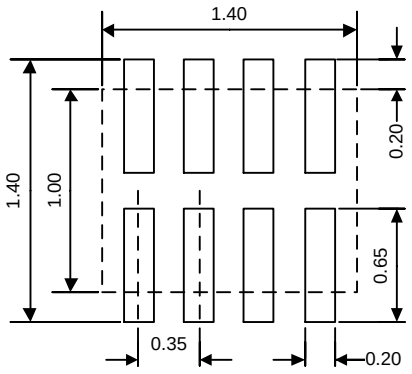
DFN8 1.4×1.0

Outline Drawing



DIMENSIONS						
Symbol	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	0.34	0.37	0.40	0.014	0.015	0.016
A1	0.00	0.02	0.05	0.000	0.0008	0.002
A3	0.100REF			0.004REF		
b	0.125	0.175	0.225	0.005	0.007	0.009
D	0.90	1.00	1.10	0.036	0.040	0.044
E	1.30	1.40	1.50	0.052	0.056	0.060
e	0.30	0.35	0.40	0.012	0.014	0.016
L	0.25	0.30	0.35	0.010	0.012	0.014
L1	0.35	0.40	0.45	0.014	0.016	0.018
M	0.100REF			0.004REF		

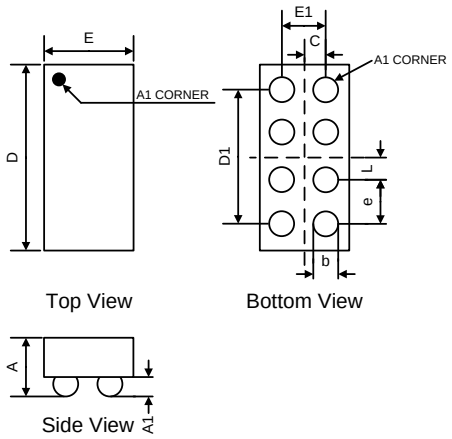
Land Pattern



NOTES:
1. Compound dimension: 1.4×1.0;
2. Unit: mm;
3. General tolerance ±0.05mm unless otherwise specified;
4. The layout is just for reference.

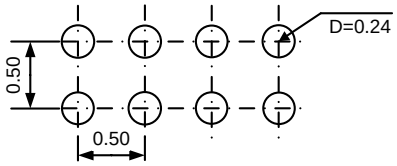
CSP8 1.9×0.9

Outline Drawing



DIMENSIONS						
Symbol	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	-	-	0.68	-	-	0.027
A1	0.21	0.231	0.24	0.0083	0.0091	0.0094
b	0.27	0.30	0.32	0.011	0.012	0.013
C	0.25BSC			0.010BSC		
D	1.85	1.90	1.95	0.073	0.075	0.077
D1	1.50BSC			0.059BSC		
E	0.85	0.90	0.95	0.033	0.035	0.037
E1	0.50BSC			0.020BSC		
e	0.50BSC			0.020BSC		
L	0.25BSC			0.010BSC		

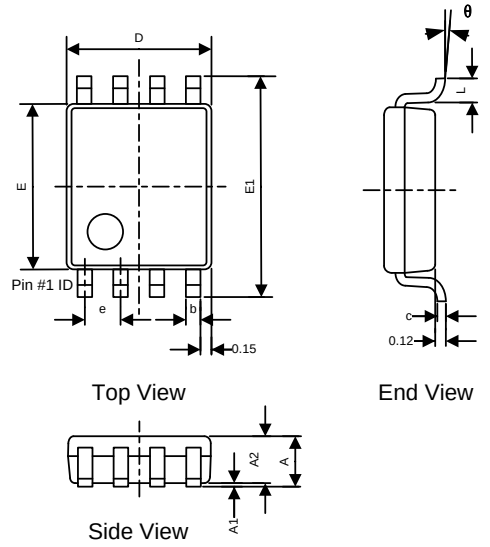
Land Pattern



NOTES:
1. Bump is Lead Free Sn/Ag/Cu.
2. Unit: mm;
3. Non-solder mask defined copper landing pad.
4. Laser Mark on silicon die back; back-lapped.

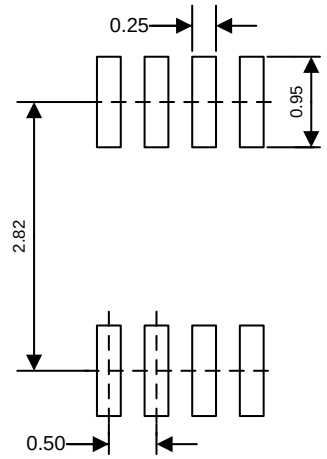
VSSOP8

Outline Drawing



Symbol	DIMENSIONS					
	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	0.60	0.75	0.90	0.023	0.029	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.60	0.70	0.80	0.023	0.027	0.031
b	0.17	-	0.27	0.007	-	0.011
c	0.08	-	0.23	0.003	-	0.009
D	1.90	2.00	2.10	0.075	0.079	0.083
E	2.20	2.30	2.40	0.086	0.090	0.094
E1	3.00	3.10	3.20	0.118	0.122	0.126
e	0.50BSC			0.020BSC		
L	0.20	0.28	0.35	0.008	0.011	0.014
θ	0 °	3 °	6 °	0 °	3 °	6 °

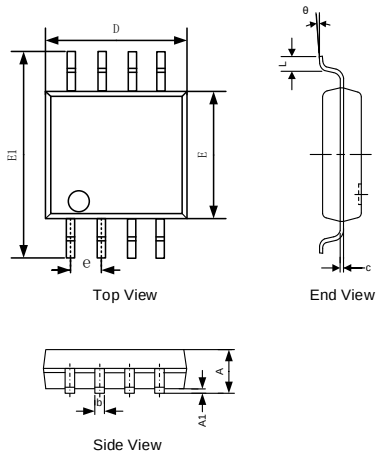
Land Pattern



NOTES:
1. Compound dimension: 2.00×2.30;
2. Unit: mm;
3. General tolerance ±0.05mm unless otherwise specified;
4. The layout is just for reference.

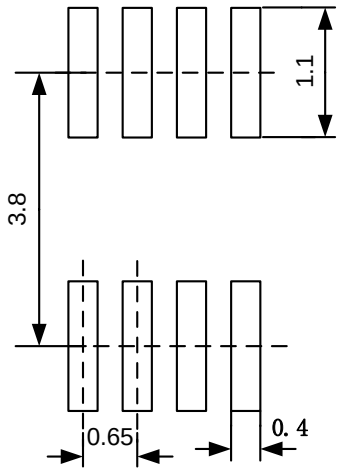
SSOP8

Outline Drawing



DIMENSIONS						
Symbol	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	1	-	1.3	0.039	-	0.051
A1	-	0.1	-	-	0.004	-
b	0.15	-	0.30	0.006	-	0.012
c	-	0.15	-	-	0.006	-
D	2.75	2.95	3.15	0.108	0.116	0.124
E	2.70	2.80	2.90	0.106	0.110	0.114
E1	3.75	4	4.25	0.147	0.157	0.167
e	0.65BSC			0.026BSC		
L	0.2	0.4	0.6	0.008	0.016	0.024
θ	0 °	-	8 °	0 °	-	8 °

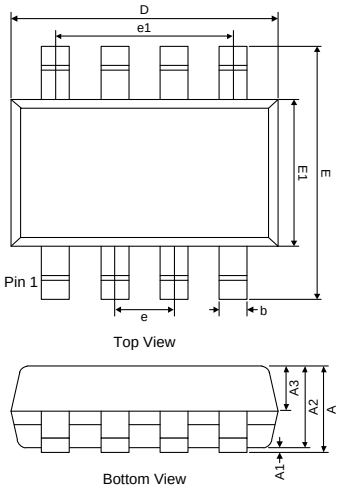
Land Pattern



NOTES:
1. Compound dimension: 2.95×2.8.
2. Unit: mm.
3. General tolerance ±0.05mm unless otherwise specified.
4. The layout is just for reference.

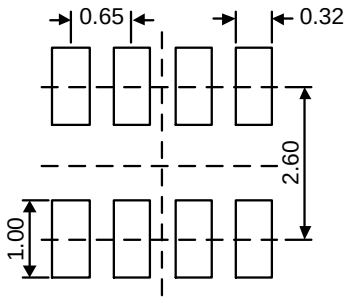
SOT23-8

Outline Drawing



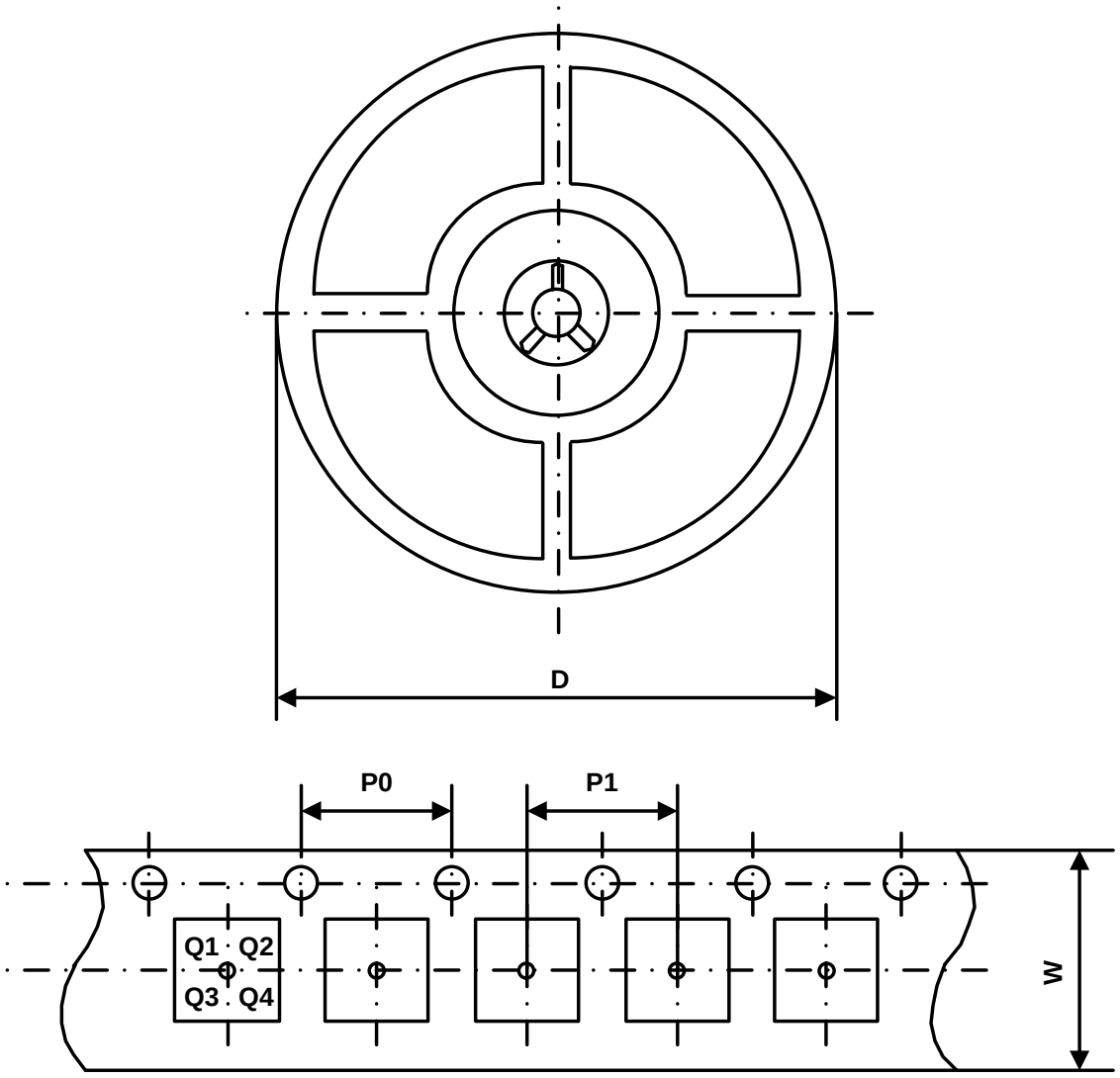
DIMENSIONS						
Symbol	MILLIMETERS			INCHES		
	Min	Typ	Max	Min	Typ	Max
A	-	-	1.10	-	-	0.044
A1	0	-	0.10	0	-	0.004
A2	0.80	0.90	1.00	0.032	0.036	0.040
A3	0.40	0.50	0.60	0.016	0.020	0.024
b	0.26	-	0.38	0.011	-	0.016
b1	0.25	0.30	0.33	0.010	0.012	0.014
c	0.14	0.15	0.16	0.005	0.006	0.007
D	2.85	2.926	2.98	0.114	0.118	0.120
E	2.65	2.80	2.95	0.106	0.112	0.118
E1	1.55	1.626	1.68	0.062	0.066	0.068
e	0.60	0.65	0.70	0.024	0.026	0.028
e1	1.85	1.95	2.05	0.074	0.078	0.082
L	0.30	0.40	0.60	0.012	0.016	0.024
L1	0.59REF			0.024REF		
L2	0.25BSC			0.010BSC		
θ	0 °	-	8 °	0 °	-	8 °
θ1	10 °	12 °	14 °	10 °	12 °	14 °
θ2	10 °	12 °	14 °	10 °	12 °	14 °

Land Pattern



NOTES:
1. Compound dimension: 2.95×2.80.
2. Unit: mm.
3. General tolerance ±0.05mm unless otherwise specified.
4. The layout is just for reference.

Packing Information



Part Number	Package Type	Carrier Width (W)	Pitch (P0)	Pitch (P1)	Reel Size (D)	PIN 1 Quadrant
UM3202Q	DFN8 1.70×1.35	8 mm	4 mm	4 mm	180 mm	Q1
UM3202QA	DFN8 1.4×1.0	8 mm	4 mm	4 mm	180 mm	Q1
UM3202H	CSP8 1.9×0.9	8 mm	4 mm	4 mm	180 mm	Q1
UM3202V	VSSOP8	8 mm	4 mm	4 mm	180 mm	Q3
UM3202A	SSOP8	12 mm	4 mm	4 mm	180 mm	Q3
UM3202S	SOT23-8	8 mm	4 mm	4 mm	180 mm	Q3

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http://www.union-ic.com/index.aspx?cat_code=RoHSDeclaration

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