
Hysteretic Buck High Brightness LED Driver with Internal Switch

UM1361S SOT23-5

General Description

The UM1361S is a PWM step-down converter with internal power switch, designed for driving single or multiple series connected LEDs efficiently from a voltage source higher than the LED voltage. The device operates from an input supply between 6V and 40V and employs hysteretic control with a high side current sense resistor to set the constant output current up to 1A. The device is well suited for applications requiring a wide input range. The high side current sensing and an integrated current sensing circuitry minimize the number of external components while delivering an accurate average output current. Depending upon supply voltage and external components, this device can provide up to 30 watts of output power.

Output current can be adjusted below the set value, by applying an external control signal to the VSET pin. The VSET pin will accept either a DC voltage or a PWM waveform. Dedicated pulse width modulation (PWM) input enables pulsed LED dimming over a wide range of brightness levels. A hysteretic control method ensures excellent input supply rejection and fast response during load transients and PWM dimming.

Applying a voltage of 0.2V or lower to the VSET pin turns the output off and switches the device into a low current standby state.

The UM1361S comes in small SOT23-5 package. It is ideal for industrial and general lighting applications.

Applications

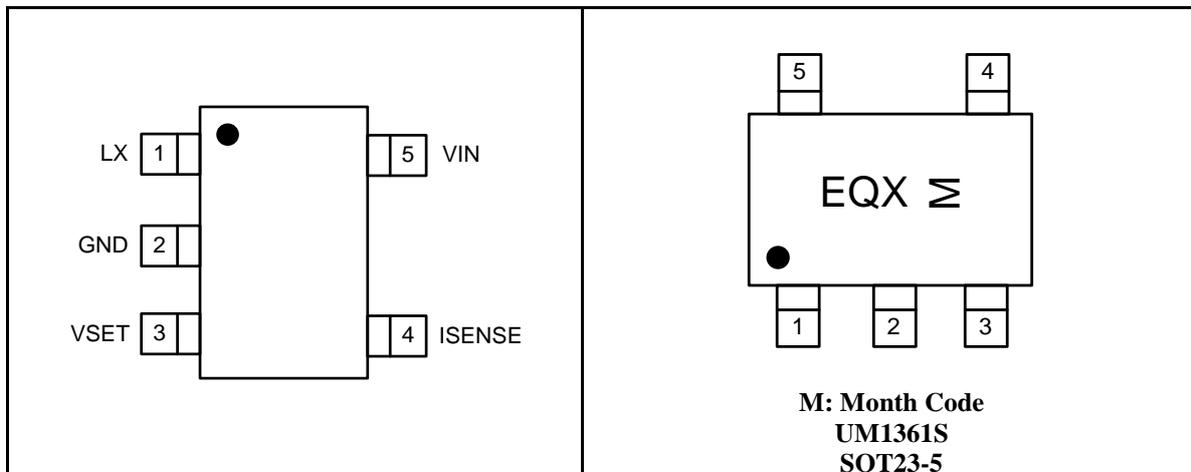
- Low Voltage Halogen Replacement LEDs
- Low Voltage Industrial Lighting
- LED Back-Side Lighting
- Illuminated Signs
- DC/DC or AC/DC LED Driver Application
- General Purpose, Constant Current Source

Features

- Hysteretic Control with High Side Current Sensing
- Integrated 40V 0.5Ω NDMOS
- > 90% Efficiency
- Wide Input Voltage Range: 6V to 40V
- ±5% LED Current Accuracy
- Adjustable Constant LED Current
- Analog or PWM Control Signal for PWM Dimming
- Over Temperature, Open Circuit LED Protection
- Up to 1MHz Switching Frequency
- Pb-Free SOT23-5 Package

Pin Configurations

Top View



Ordering Information

Part Number	Packaging Type	Marking Code	Shipping Qty
UM1361S	SOT23-5	EQX	3000pcs/7Inch Tape & Reel

Pin Description

Pin Number	Symbol	Function
1	LX	Drain of NDMOS switch.
2	GND	Ground (0V).
3	VSET	Multi-function On/Off and brightness control pin. Leave floating for normal operation. Drive to voltage below 0.2V to turn off output current. Drive with DC voltage ($0.3V < VSET < 2.5V$) to adjust output current from 12% to 100% of I_{OUTnom} . Drive with PWM signal from open-collector or open-drain transistor, to adjust output current. Adjustment range 1% to 100% of I_{OUTnom} for $f < 500Hz$ and 2% to 100% of I_{OUTnom} for $f > 20kHz$.
4	ISENSE	Connect resistor R_S from this pin to VIN to define nominal average output current $I_{OUTnom} = 0.1/R_S$.
5	VIN	Input voltage (6V to 40V). Decouple to ground with 10 μ F or higher X7R ceramic capacitor close to device.

Absolute Maximum Ratings

Over operating free-air temperature (unless otherwise noted) (Note 1)

Symbol	Parameter	Value	Unit
V_{IN}	Input Voltage Range	-0.3 to +45	V
V_{LX} , V_{ISENSE}	Voltages on LX, ISENSE	-0.3 to +45	V
V_{SET}	V_{SET} Pin Voltage	-0.3 to +6	V
θ_{JA}	Thermal Resistance (Junction to Ambient)	250	°C/W
θ_{JC}	Thermal Resistance (Junction to Case)	130	°C/W
T_J	Maximum Junction Temperature	+150	°C
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_L	Lead Temperature for Soldering 10 Seconds	+260	°C

Note 1: These are stress ratings only and functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability. All voltage values are with respect to network ground terminal.

Recommended Operating Conditions

Symbol	Parameter	Min	Typ	Max	Unit
V_{IN}	Input Voltage Range	6.0		40	V
T_A	Operating Ambient Temperature	-40		85	°C
T_J	Operating Junction Temperature	-40		150	°C

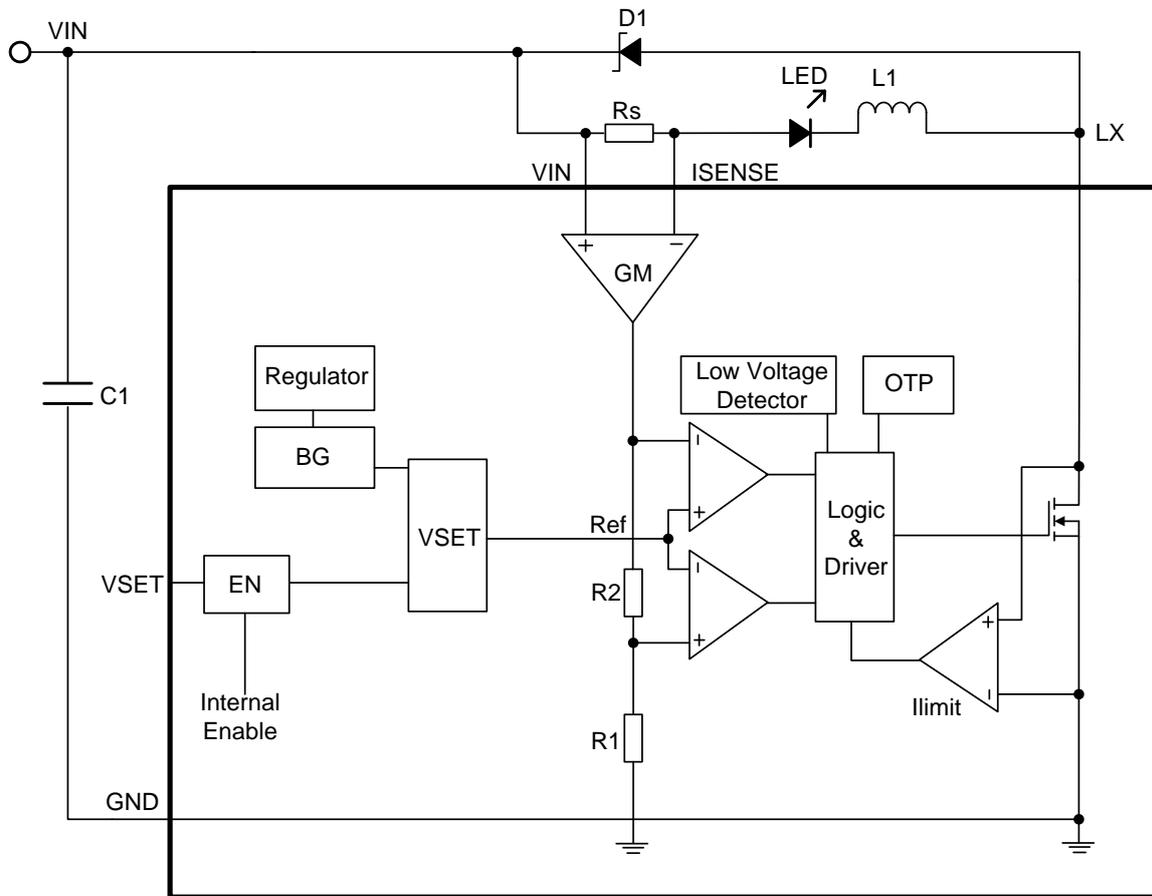
Electrical Characteristics

 ($V_{IN}=16V$, $T_A=25\text{ }^\circ\text{C}$, unless otherwise noted)

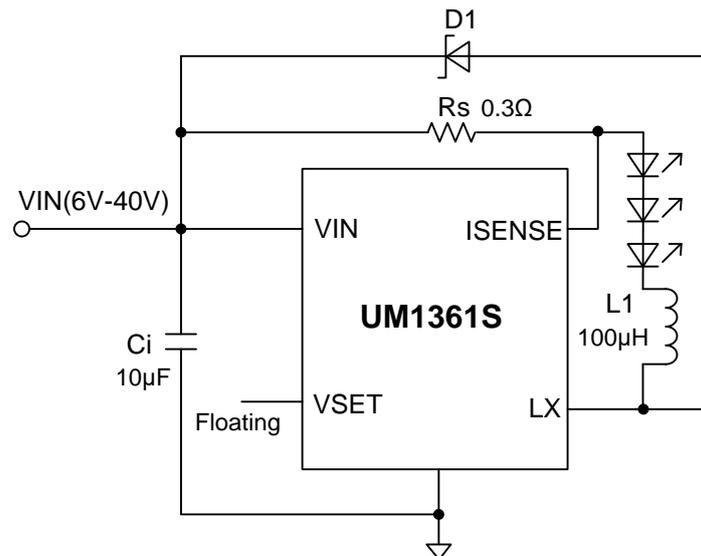
Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{IN}	Input Voltage Range		6		40	V
I_{LED}	Output Current	$R_S=0.3\Omega$		333		mA
		$R_S=0.1\Omega$		1		A
I_Q	Quiescent Current without Switching	VSET Pin Floating, $V_{IN}=16V$		0.6		mA
I_{SD}	Shutdown Current	VSET Pin Grounded		37	50	μA
V_{SENSE}	Mean Current Sense Threshold Voltage	Measured on ISENSE Pin with Respect to V_{IN}	95	100	105	mV
V_{SENSE_HYS}	Sense Threshold Hysteresis			± 13		%
I_{SENSE}	ISENSE Pin Input Current	$V_{SENSE}=V_{IN}-0.1$		16		μA
V_{en}	V_{SET} Range on VSET Pin	For DC Dimming	0.3		2.5	V
V_{enon}	DC Voltage on VSET Pin to Enable	V_{en} Rising		0.25		V
V_{enoff}	DC Voltage on VSET Pin to Disable	V_{en} Falling		0.2		V
R_{LX}	LX Switch on Resistance	$I_{LX}=100\text{mA}$		0.5		Ω
$I_{LX(leak)}$	LX Switch Leakage Current				5	μA
F_{LX}	Operating Frequency	$V_{IN}=16V$, $V_{OUT}=9.6V$ (3LEDs), $L=47\mu\text{H}$, $\Delta I=0.25A$ ($I_{LED}=1A$)		233		kHz
F_{LXmax}	Recommended Maximum Switch Frequency				1.0	MHz
T_{on_rec}	Recommended Minimum Switch ON Time	For 4% Accuracy		500		ns
	Max Duty Cycle			98		%
D_{LX}	Recommended Duty Cycle Range		25		75	%
T_{PD} (Note 2)	Internal Comparator Propagation Delay			45		ns
T_{OTP}	Over Temperature Protection			155		$^\circ\text{C}$
T_{OTP_Hys}	Temperature Protection Hysteresis			30		$^\circ\text{C}$
I_{XLmax}	Current Limit	Peak Inductor Current	1.5			A

Note 2: Parameters are not tested at production, but guaranteed by design.

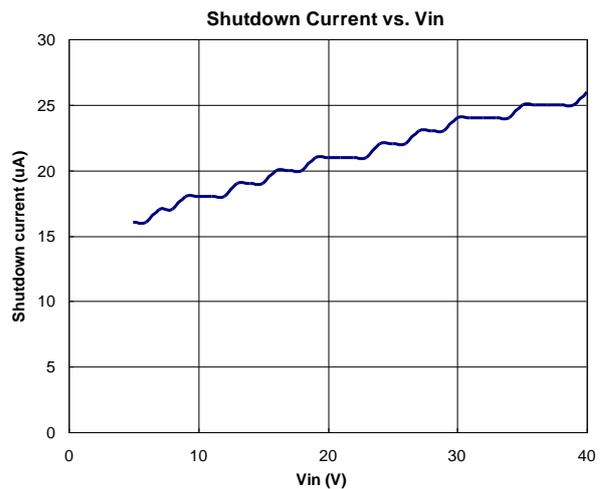
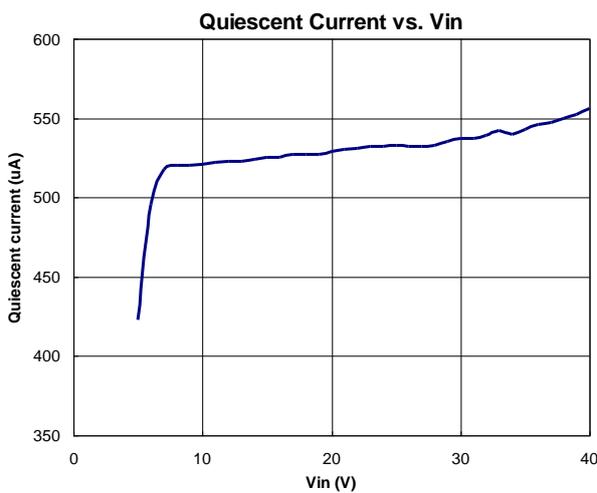
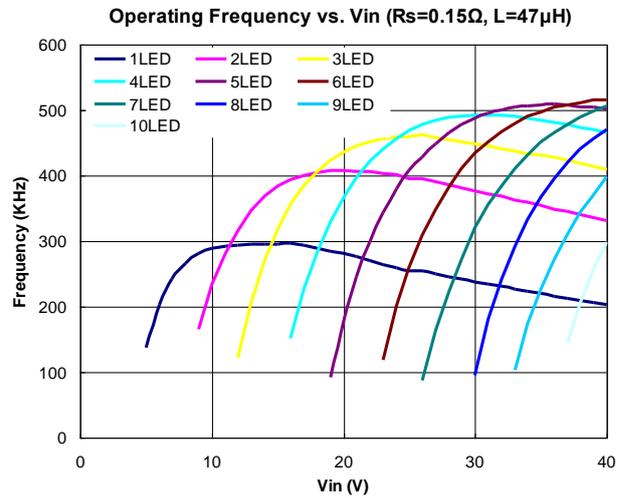
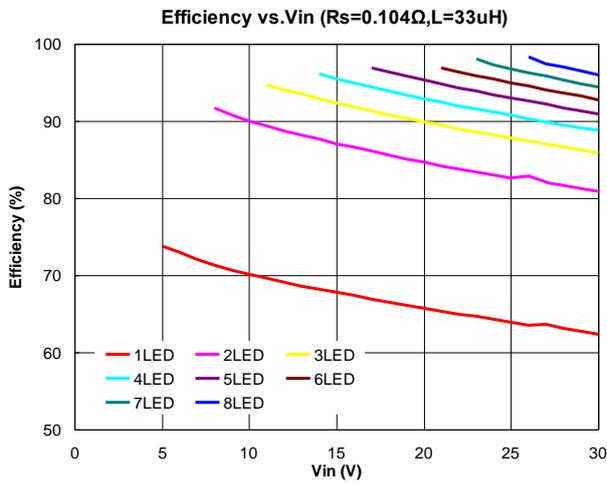
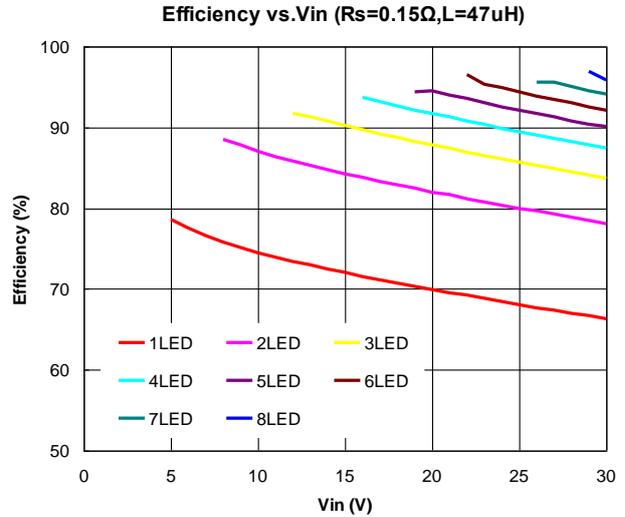
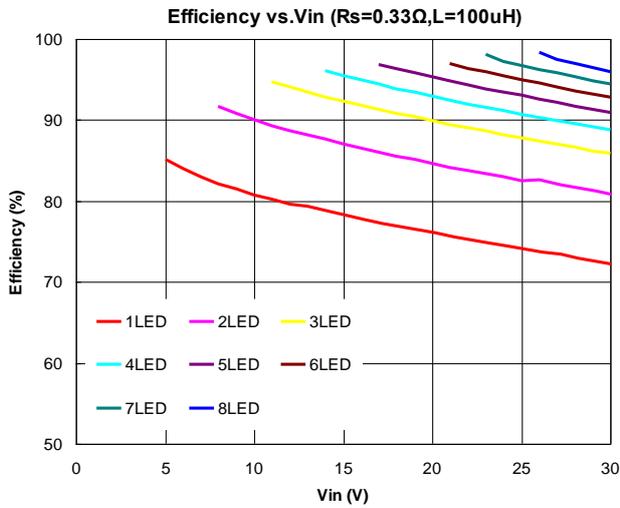
Function Block Diagram



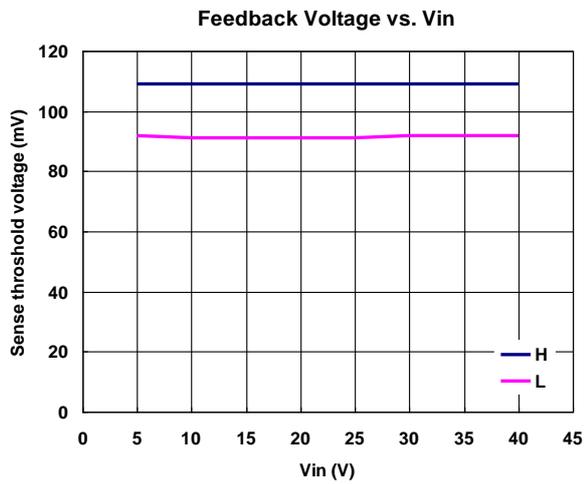
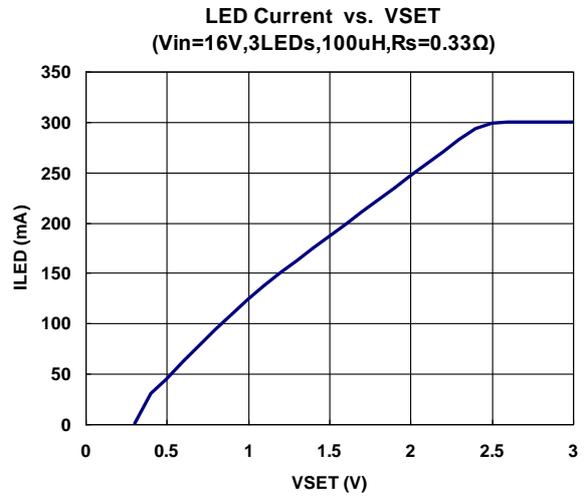
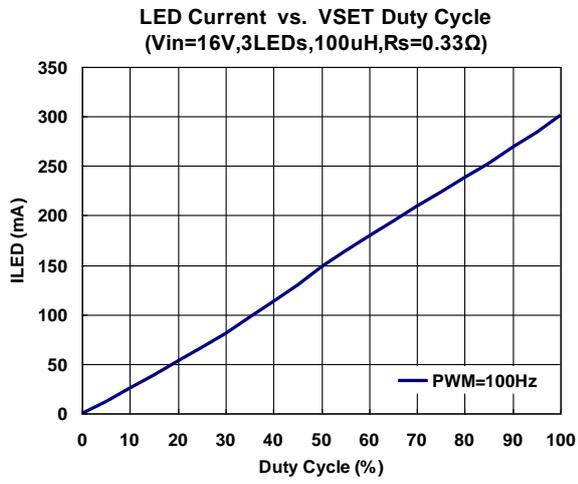
Typical Application Circuit



Typical Operating Characteristics

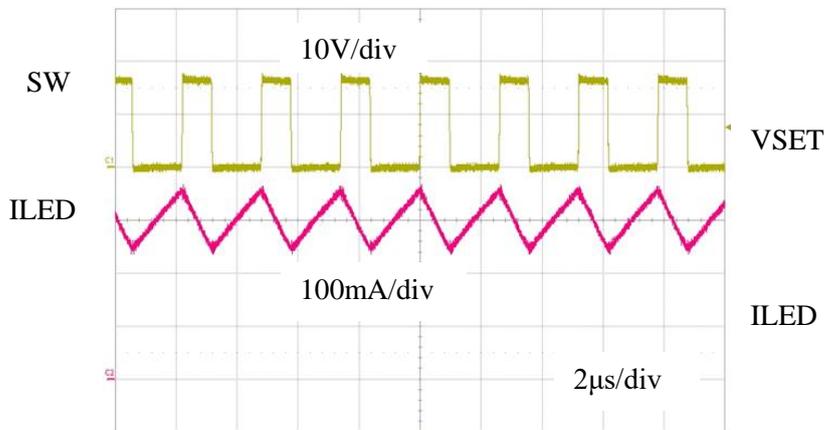


Typical Operating Characteristics (Continued)

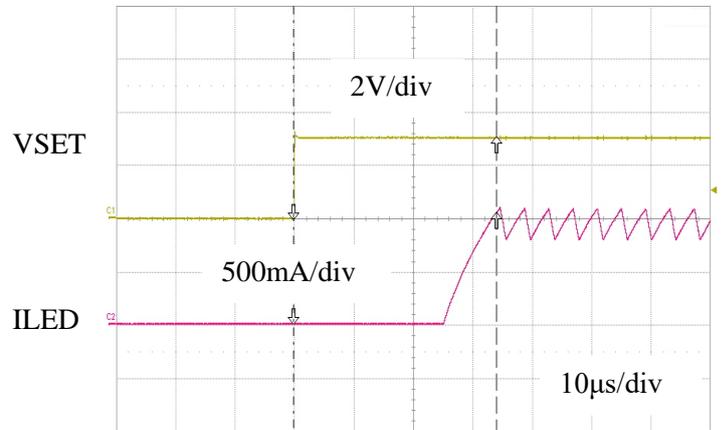


Typical Operating Characteristics (Continued)

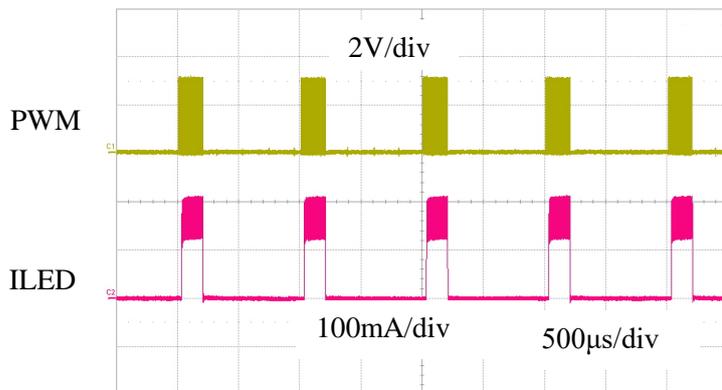
Steady State Waveforms
(3LEDs, 100 μ H, V_{in} =16V, R_s =0.33 Ω)



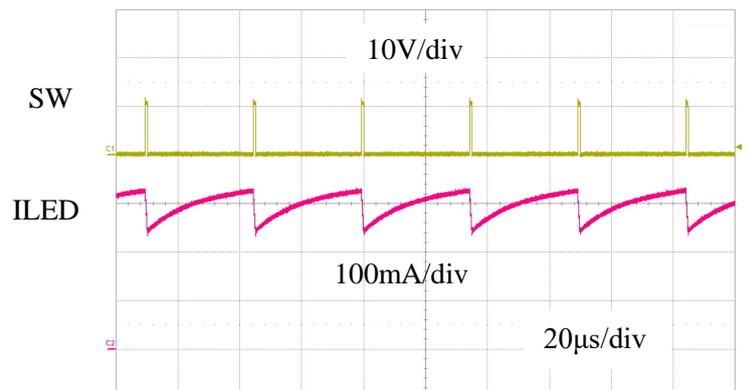
Start up Waveforms



Dimming Waveforms
(PWM=50%, 3LEDs, 100 μ H, V_{in} =16V, R_s =0.33 Ω)



Pulse Skip Mode
(3LEDs, 100 μ H, V_{in} =10V, R_s =0.33 Ω)



Applications Information

Setting Nominal Average Output Current with External Resistor R_S

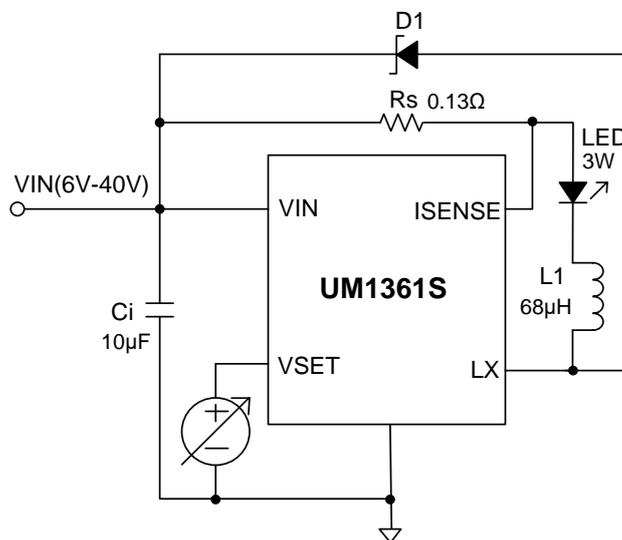
The nominal average output current in the LED(s) is determined by the value of the external current sense resistor (R_S) connected between VIN and ISENSE and is given by:

$$I_{OUTnom} = 0.1/R_S \quad (R_S \geq 0.1\Omega)$$

This equation is valid when VSET pin is float or applied with a voltage higher than 2.5V (must be less than 5V). Actually, R_S sets the maximum average current which can be adjusted to a less one by dimming.

Output Current Adjustment by External DC Control Voltage

The VSET pin can be driven by an external dc voltage (V_{DIM}), as shown, to adjust the output current to a value below the nominal average value defined by R_S .



The average output current is given by:

$$I_{OUT} = (0.1 * V_{DIM}) / (2.5 * R_S) \quad [for \ 0.3V < V_{DIM} < 2.5V]$$

Note that 100% brightness setting corresponds to:

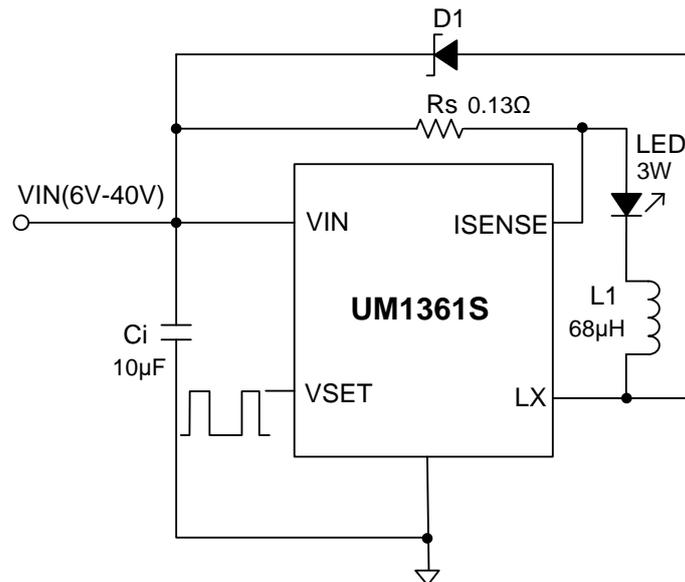
$$(2.5V \leq V_{DIM} \leq 5V)$$

Output Current Adjustment by PWM Control

A Pulse Width Modulated (PWM) signal with duty cycle PWM can be applied to the VSET pin, as shown below, to adjust the output current to a value below the nominal average value set by resistor R_S :

$$I_{OUT} = (0.1 * D) / R_S \quad (0 \leq D \leq 100\%, \ 2.5V < V_{pulse} < 5V)$$

$$I_{OUT} = (V_{pulse} * 0.1 * D) / (2.5 * R_S) \quad (0 \leq D \leq 100\%, \ 0.5V < V_{pulse} < 2.5V)$$



PWM dimming provides reduced brightness by modulating the LED's forward current between 0% and 100%. The LED brightness is controlled by adjusting the relative ratios of the on time to the off time. A 25% brightness level is achieved by turning the LED on at full current for 25% of one cycle. To ensure this switching process between on and off state is invisible by human eyes, the switching frequency must be greater than 100 Hz. Above 100 Hz, the human eyes average the on and off times, seeing only an effective brightness that is proportional to the LED's on-time duty cycle. The advantage of PWM dimming is that the forward current is always constant, therefore the LED color does not vary with brightness as it does with analog dimming. Pulsing the current provides precise brightness control while preserving the color purity.

Capacitor Selection

A low ESR capacitor should be used for input decoupling, as the ESR of this capacitor appears in series with the supply source impedance and lowers overall efficiency. This capacitor has to supply the relatively high peak current to the coil and smooth the current ripple on the input supply. A minimum value of 4.7µF is acceptable if the input source is close to the device, but higher values will improve performance at lower input voltages, especially when the source impedance is high. The input capacitor should be placed as close as possible to the IC.

For maximum stability over temperature and voltage, capacitors with X7R, X5R, or better dielectric are recommended. Capacitors with Y5V dielectric are not suitable for decoupling in this application and should NOT be used.

Inductor Selection

Recommended inductor values for the UM1361S are in the range 27µH to 100µH. Higher values of inductance are recommended at lower output current in order to minimize errors due to switching delays, which result in increased ripple and lower efficiency. Higher values of inductance also result in a smaller change in output current over the supply voltage range.

The inductor should be mounted as close to the device as possible with low resistance connections to the SW and VIN pins. The chosen coil should have a saturation current higher than the peak output current and a continuous current rating above the required mean output current. Following table gives the guideline on inductor selection:

Load Current	Inductor	Saturation Current
$I_{OUT} > 1A$	27-47 μ H	1.3-1.5 Times of Load Current
$0.8A < I_{OUT} \leq 1A$	33-82 μ H	
$0.4A < I_{OUT} \leq 0.8A$	47-100 μ H	
$I_{OUT} \leq 0.4A$	68-220 μ H	

The inductor value should be chosen to maintain operating duty cycle and switch 'on'/off times within the specified limits over the supply voltage and load current range. The following equations can be used as a guide.

SW Switch 'On' time

$$T_{ON} = (L * \Delta I) / (V_{IN} - V_{LED} - I_{LED} * (R_S + R_L + R_{LX}))$$

SW Switch 'Off' time

$$T_{OFF} = (L * \Delta I) / (V_{LED} + V_D + I_{LED} * (R_S + R_L))$$

Where:

L is the coil inductance (H)

R_L is the coil resistance (Ω)

R_S is the current sense resistance (Ω)

I_{LED} is the required LED current (A)

ΔI is the coil peak-peak ripple current (A) {Internally set to $0.25 \times I_{LED}$ }

V_{IN} is the supply voltage (V)

V_{LED} is the total LED forward voltage (V)

R_{LX} is the switch resistance (Ω) {=0.3 Ω nominal}

V_D is the diode forward voltage at the required load current (V)

Diode Selection

For maximum efficiency and performance, the rectifier (D1) should be a fast low capacitance Schottky diode with low reverse leakage at the maximum operating voltage and temperature. They also provide better efficiency than silicon diodes, due to a combination of lower forward voltage and reduced recovery time.

It is important to select parts with a peak current rating above the peak coil current and a continuous current rating higher than the maximum output load current. It is very important to consider the reverse leakage of the diode when operating above 85 °C. Excess leakage will increase the power dissipation in the device and if close to the load may create a thermal runaway condition.

The higher forward voltage and overshoot due to reverse recovery time in silicon diodes will increase the peak voltage on the LX output. If a silicon diode is used, care should be taken to ensure that the total voltage appearing on the LX pin including supply ripple, does not exceed the specified maximum value.

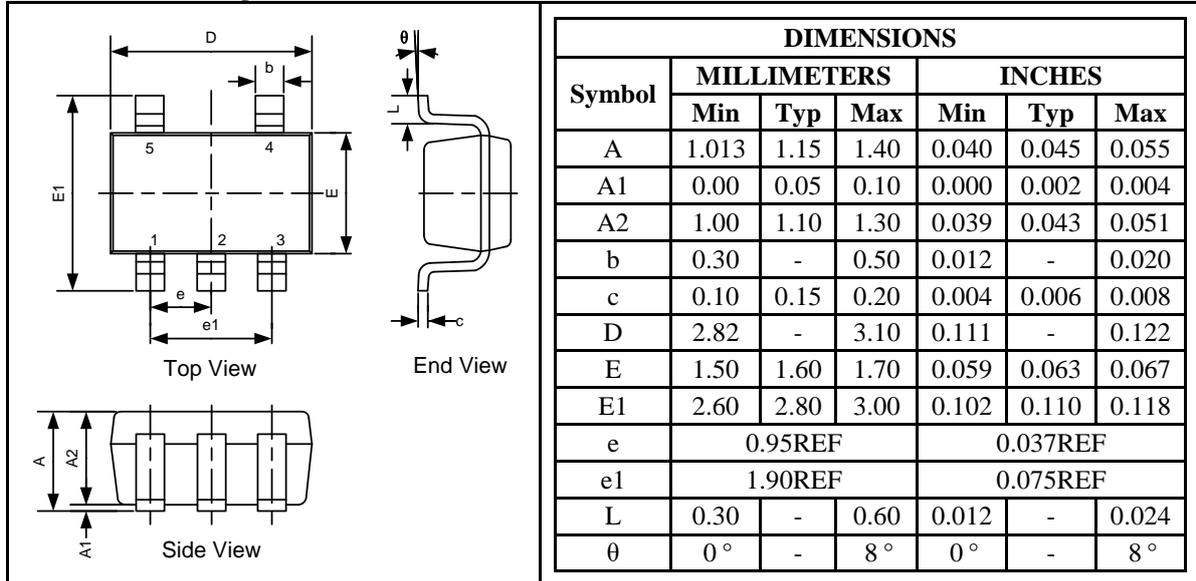
PCB Layout Guidelines

Careful PCB layout is critical to achieve low switching losses and stable operation. Minimize ground noise by connecting high current ground returns, the input bypass capacitor ground lead, and the output filter ground lead to a single point. Place R_{sense} as close as possible to the I_{sense} and V_{IN} . For better noise immunity, a Kelvin connection is strongly recommended between I_{sense} and R_{sense} .

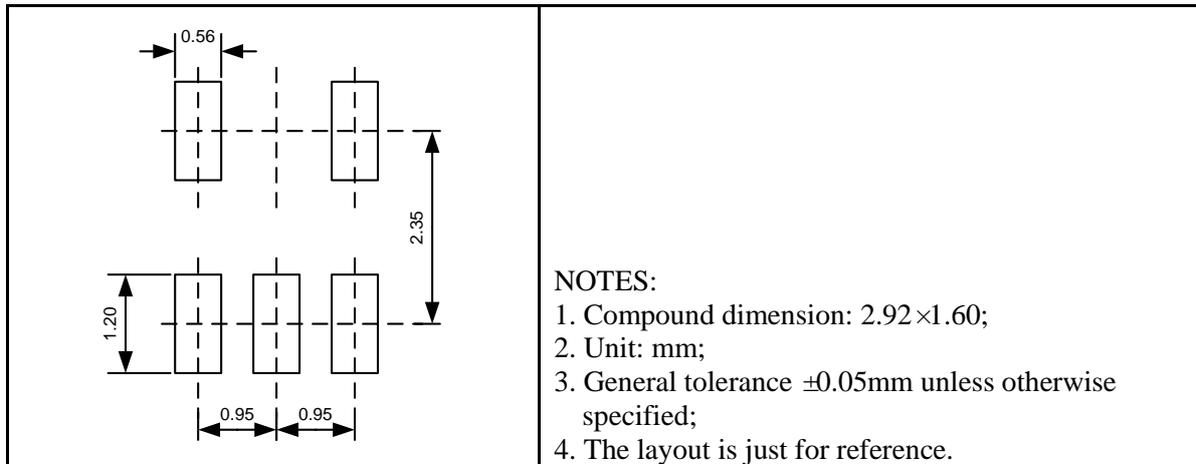
Package Information

SOT23-5

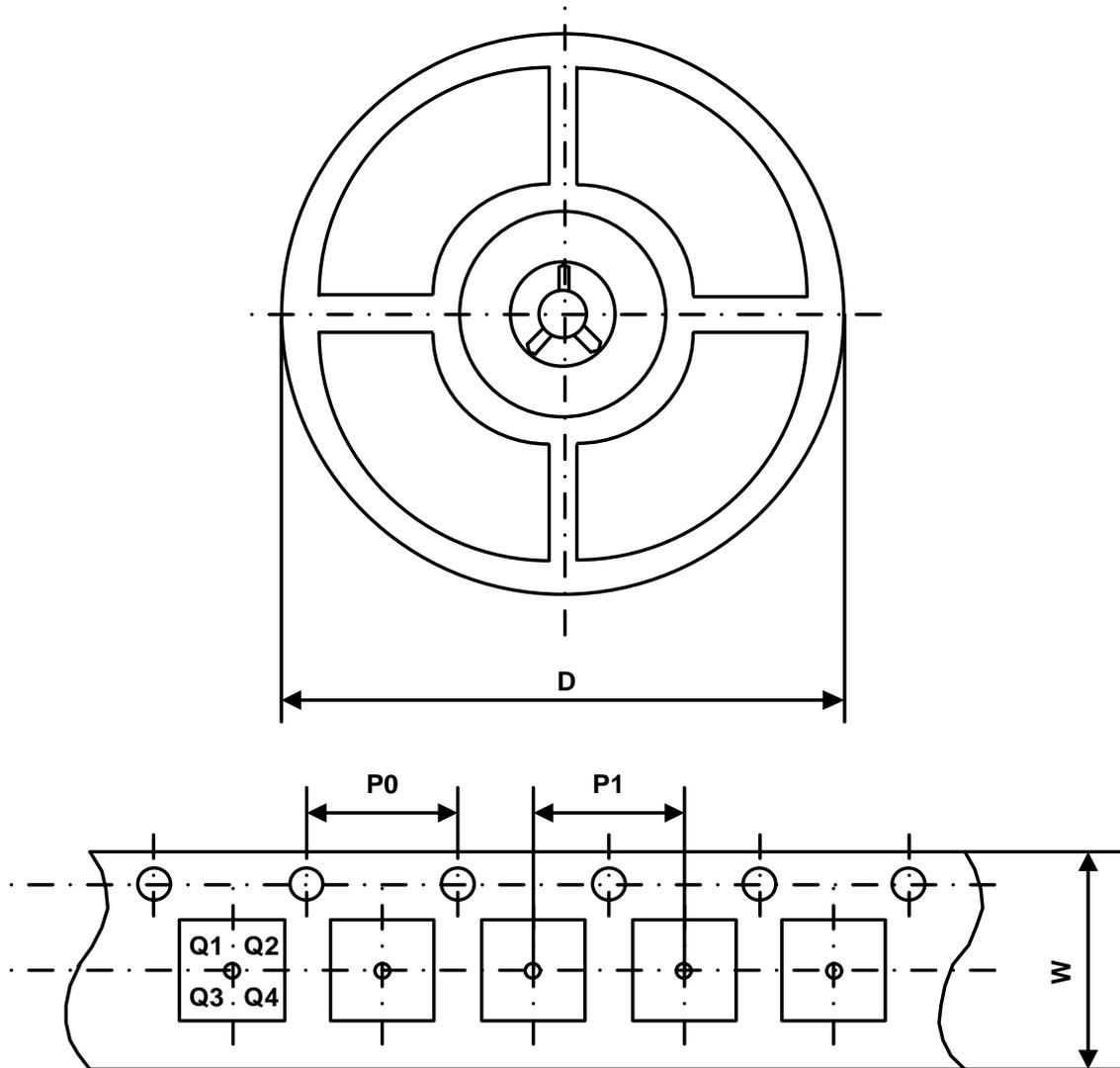
Outline Drawing



Land Pattern



Packing Information



Part Number	Package Type	Carrier Width (W)	Pitch (P0)	Pitch (P1)	Reel Size (D)	PIN 1 Quadrant
UM1361S	SOT23-5	8 mm	4 mm	4 mm	180 mm	Q3

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