Dual Line ESD Protection Diode Array

UESD6V8S2B SOT523

General Description

The UESD6V8S2B of TVS diode array is designed to protect sensitive electronics from damage or latch-up due to ESD. For use in applications where board space is at a premium. It is unidirectional device and may be used on lines where the signal polarities are above ground, each device will protect up to two lines.

TVS diodes are solid-state devices feature large cross-sectional area junctions for conducting high transient currents, specifically for transient suppression. It offers desirable characteristics for board level protection including fast response time, low operating, low clamping voltage, and no device degradation.

The UESD6V8S2B may be used to meet the immunity requirements of IEC 61000-4-2, ±15kV air, ±8kV contact discharge and MIL-STD-883 METHOD 3015, ±8 KV HBM. The small package makes them ideal for use in portable electronics such as cell phones, PDA’s, notebook computers, and digital cameras.

Applications

- Cellular Handsets & Accessories
- Cordless Phones
- Personal Digital Assistants (PDA’s)
- Notebooks & Handhelds
- Portable Instrumentation
- Digital Cameras
- Peripherals
- MP3 Players

Features

- Transient protection for data & power lines to
  - IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
  - MIL-STD-883 3015 (HBM) ±8 kV
- Protect two I/O lines
- Working Voltages: 5V
- Low Leakage current
- Low operating and clamping voltage
- Solid-state silicon avalanche technology

Pin Configurations

Top View

M: Monthly Code
UESD6V8S2B
SOT523
### Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Working Voltage</th>
<th>Packaging Type</th>
<th>Channel</th>
<th>Marking Code</th>
<th>Shipping Qty</th>
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</thead>
<tbody>
<tr>
<td>UESD6V8S2B</td>
<td>5.0V</td>
<td>SOT523</td>
<td>2</td>
<td>5ZA</td>
<td>3000/7 Inch Reel</td>
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</table>

### Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>RATING</th>
<th>SYMBOL</th>
<th>VALUE</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Peak Pulse Power (tp = 8/20 μs)</td>
<td>PPK</td>
<td>140</td>
<td>Watts</td>
</tr>
<tr>
<td>Peak Pulse Current (tp = 8/20 μs)</td>
<td>IPP</td>
<td>11</td>
<td>A</td>
</tr>
<tr>
<td>Lead Soldering Temperature</td>
<td>TL</td>
<td>260(10 sec.)</td>
<td>°C</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>T_J</td>
<td>-55 to +125</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>T_STG</td>
<td>-55 to +125</td>
<td>°C</td>
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</table>

### Electrical Characteristics

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reverse Stand-Off Voltage</td>
<td>V_RWM</td>
<td>It = 1mA</td>
<td>6</td>
<td>5</td>
<td>V</td>
<td>V</td>
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<tr>
<td>Reverse Breakdown Voltage</td>
<td>V_BR</td>
<td>V_RWM = 5V, T=25°C</td>
<td>6.8</td>
<td>7.2</td>
<td>V</td>
<td>V</td>
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<tr>
<td>Reverse Leakage Current</td>
<td>I_R</td>
<td>IPP = 5A, I_p = 8/20μS</td>
<td>9.1</td>
<td>V</td>
<td>V</td>
<td>V</td>
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<tr>
<td>Clamping Voltage</td>
<td>V_C</td>
<td>IPP = 11A, I_p = 8/20μS</td>
<td>13</td>
<td>pF</td>
<td>pF</td>
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<tr>
<td>Junction Capacitance</td>
<td>C_J</td>
<td>Pin 1,2 to 3 V_R = 0V, f = 1MHz</td>
<td>40</td>
<td>50</td>
<td>pF</td>
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<tr>
<td>Junction Capacitance</td>
<td>C_J</td>
<td>Pin 1,2 to 3 V_R = 2.5V, f = 1MHz</td>
<td>30</td>
<td>40</td>
<td>pF</td>
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<tr>
<td>Reverse dynamic resistance</td>
<td>R_dyn,rev</td>
<td>IPP = 1A~5A</td>
<td>0.6</td>
<td>Ω</td>
<td>Ω</td>
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<tr>
<td>Forward dynamic resistance</td>
<td>R_dyn,fwd</td>
<td>IPP = 1A~5A</td>
<td>0.5</td>
<td>Ω</td>
<td>Ω</td>
<td></td>
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</tbody>
</table>
UESD6V8S2B

Typical Operating Characteristics

Non-Repetitive Peak Pulse Power vs. Pulse Time

Clamping Voltage vs. Peak Pulse Current

Forward Voltage vs. Forward Current

Junction Capacitance vs. Reverse Voltage

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UESD6V8S2B

Applications Information

UESD6V8S2B ESD protection diode is designed to protect dual data, I/O, or power supply line. The device is unidirectional and may be used on lines where the signal polarity is above ground. The cathode should be placed towards the line that is to be protected.

Device Connection for Protection of Dual Data Lines

The Dual TVS Diode Array is designed to protect up to two unidirectional data lines. The device is connected as follows:
Unidirectional protection of two I/O lines is achieved by connecting pins 1 and 2 to the data lines. Pin 3 is connected to ground. The ground connection should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.

Circuit Board Layout Recommendations for Suppression of ESD

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:
Place the TVS near the input terminals or connectors to restrict transient coupling.
Minimize the path length between the TVS and the protected line.
Minimize all conductive loops including power and ground loops.
The ESD transient return path to ground should be kept as short as possible.
Never run critical signals near board edges.
Use ground planes whenever possible. For multilayer printed-circuit boards, use ground vias.
Keep parallel signal paths to a minimum.
Avoid running protection conductors in parallel with unprotected conductor.
Minimize all printed-circuit board conductive loops including power and ground loops.
Avoid using shared transient return paths to a common ground point.
Package Information
UESD6V8S2B SOT523

Outline Drawing

NOTES:
1. Compound dimension: 1.60×0.80;
2. Unit: mm;
3. General tolerance ±0.05mm unless otherwise specified;
4. The layout is just for reference.

Land Pattern

Tape and Reel Orientation
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